

CHIP TO CHIP INTERFACE

Abstract

A chip to chip interface comprises a driver configured to provide a first signal in response to a change in first data at one edge of a clock signal and a second signal in response to a change in second data at another edge of the clock signal. The chip to chip interface comprises a receiver configured to receive the first signal and the second signal and toggle a first bit in response to the first signal and toggle a second bit in response to the second signal.